

Notice of References Cited	Application/Control No. 10/500,654	Applicant(s)/Patent Under Reexamination PUECH ET AL.	
	Examiner Mahmoud Dahimene	Art Unit 1765	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,501,893	03-1996	Laermer et al.	428/161
*	B	US-6,846,746	01-2005	Rattner et al.	438/706
*	C	US-6,277,756	08-2001	Ohara et al.	438/700
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	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	S. Wolf and R.N. Ttauber, Silicon Processing for the VLSI Era, Volume 1- Process Technology, Lattice Press, 1986 (pages 544-545)
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.